

## EAST Search History (Prior Art)

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	0	(transferring data block flash memory memory controller computing ECC selectively storing plurality registers switching mechanism).clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2010/06/07 09:19
L2	11539	(714/746,710,758,763,5,6,8).ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2010/06/07 09:20
L3	108	(transferring data block flash memory memory controller computing ECC selectively storing plurality registers switching)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2010/06/07 09:21
L4	6	2 and 3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2010/06/07 09:21
S1	967	transfer\$4 same data same block same Flash\$4 near4 memory same controller	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/08/22 11:27
S2	2499	(determin\$4 or comput\$4 or generat\$4) same ECC same data near4 block	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/08/22 11:28
S3	166	stor\$4 same ECC same register same switch\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/08/22 11:29
S4	14	S1 and S2 and S3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/08/22 11:29
S7	14	S4 and transfer\$4 same data same block	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/08/22 11:32

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S8	14	S7 and error	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/08/22 11:32
S9	14	S8 and stor\$4 same transfer\$4 near4 data	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/08/22 11:39
S10	1	controller shift data block between flash memory controller ECC data block	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	SAM E	ON	2008/08/22 11:52
S11	1	controller shift\$4 data block between flash memory controller ECC data block	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	SAM E	ON	2008/08/22 11:53
S12	93	controller data block between flash memory controller ECC data block	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	SAM E	ON	2008/08/22 11:53
S13	23	("6,906,961" "6,683,817" "20040221098")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/08/22 12:23
S14	9	S13 AND STOR\$4 SAME TRANSFER\$4 SAME DATA	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/08/22 12:23
S15	0	S14 and control\$4 same shift\$4 same data	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/08/22 12:41
S16	1	S10 and control\$4 same shift\$4 same data	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/08/22 12:42
S17	3	S13 and control\$4 same shift\$4 same data	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/08/22 12:51

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S18	4	S13 and control\$4 same shift\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/08/22 12:54
S19	101559	control\$4 same shift\$4 same data	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/08/22 12:57
S20	214	S1 and S19	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/08/22 12:57
S21	24	S20 and controller same shift\$4 same block same flash near4 memory	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/08/22 12:59
S22	2	"6,594,792".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/08/22 20:38
S23	0	S22 and exclusive-or	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/08/22 20:38
S24	0	S22 and exclusive	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/08/22 20:38
S25	0	S22 and "XOR"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/08/22 20:38
S26	1	S22 and function	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/08/22 20:41
S27	5	comput\$4 ECC performing exclusive-or function	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	SAM E	ON	2008/08/22 20:43

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S28	2	"6883131".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	SAM E	ON	2008/08/22 20:47
S29	28	ECC with exclusive-or function	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	SAM E	ON	2008/08/22 20:48
S30	0	S28 and S29	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	SAM E	ON	2008/08/22 20:48
S31	2	S28 and (exclusive-or or "XOR")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	SAM E	ON	2008/08/22 20:48
S32	0	comput\$4 same ECC same data adj bloc	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/08/22 21:20
S33	251	comput\$4 same ECC same data adj block	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/08/22 21:20
S34	5	S33 and stor\$4 same ECC same (register or memory) same switch\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/08/22 21:21
S35	40819	(transfer\$4 or sav\$4 or plac\$4 or add\$4) near5 data near4 block same between (flash adj memory same controller)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/08/22 21:27
S36	80	(transfer\$4 or sav\$4 or plac\$4 or add\$4) near5 data near4 block same between same (flash adj memory same controller)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/08/22 21:27
S37	63	comput\$4 near5 ECC same transfer\$4 near5 data	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/08/22 21:28

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S38	1	S36 and S37	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/08/22 21:28
S39	3596	ECC same transfer\$4 near5 data	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/08/22 21:30
S40	63	S37 and S39	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/08/22 21:30
S41	1	stor\$4 near4 ECC same plural\$4 near4 register near5 switch\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/08/22 21:31
S42	2	stor\$4 near4 ECC same plural\$4 near4 (register or memory) near5 switch\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/08/22 21:31
S43	28	stor\$4 near4 ECC same (register or memory) near5 switch\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/08/22 21:32
S44	1	S40 and S43	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/08/22 21:32
S45	139	stor\$4 same (error adj correction adj code or ECC) same (register or memory) near5 switch\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/08/22 21:34
S46	1	S40 and S45	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/08/22 21:35
S47	455281	stor\$4 near4 (ECC or error adj correction adj code) near6 registersame transfer\$4 near4 data	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/08/22 21:43

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S48	27	stor\$4 near4 (ECC or error adj correction adj code) near6 register same transfer\$4 near4 data	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/08/22 21:43
S49	1	stor\$4 near4 (ECC or error adj correction adj code) near6 register same transfer\$4 near4 data same shift\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/08/22 21:47
S50	9	(ECC or error adj correction adj code) near6 register same transfer\$4 near4 data same shift\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/08/22 21:47
S51	0	S50 and controller same shift\$4 same data	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/08/22 21:51
S52	9	S50 and shift\$4 same data	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/08/22 21:57
S53	5	S52 and controller	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/08/22 21:57
S54	0	S53 and flash	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/08/22 21:59
S55	1	2004/0221098	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/08/22 22:02
S56	1	S55 and switch	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/08/22 22:02
S57	3	"20040221098"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/08/22 22:04

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S58	2		S57 and ECC	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/08/22 22:04
S59	2		S58 and ECC	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/08/22 22:05
S60	2		S59 and switch\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/08/22 22:05
S61	141	flash near memory near controller near4 coupled		US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/14 15:29
S62	486	switch near controller near coupled		US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/14 15:30
S63	0		S61 and S62	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/14 15:30
S64	0	flash near memory near controller near4 coupled near switch		US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/14 15:30
S65	0	flash near memory near controller near coupled near switch		US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/14 15:31
S66	23	memory near controller near coupled near switch		US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/14 15:31
S67	49		S61 and error near4 correction	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/14 15:40

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S68	23	S67 and switch\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/14 16:02
S69	43	memory near controller near (coupled or connect\$4 or attach\$4) near switch	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/14 16:07
S70	8	S69 and (ECC or error adj correction adj circuit\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/14 16:08
S71	209992	(flash\$4 near memory)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/14 16:10
S72	15810	S71 and controller same (coupled or connect\$4) same flash near memory	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/14 16:11
S73	1889974	S72 and switch near4 (coupled or connect\$4) controller	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/14 16:13
S74	331	S72 and switch near4 (coupled or connect\$4) near4 controller	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/14 16:13
S75	23683	S74 and controll\$4 same shift\$4 data same flash near4 memory same controller	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/14 16:14
S76	4	S74 and controll\$4 same shift\$4 same data same flash near4 memory same controller	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/14 16:14
S77	23812	(flash and memory).ti.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/14 16:17



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S78	3134	S77 and controller same flash adj memory	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/14 16:18
S79	5	S78 and controller same shift\$4 same data same flash adj memory same controller same ECC	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/14 16:19
S80	5	S79 and transfer\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/14 16:31
S81	3	S79 and transfer\$4 same data	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/14 16:32
S82	3	S81 and shift\$4 same data	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/14 16:32
S83	3	S82 and block	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/14 16:44
S84	3	S82 and block same data	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/14 16:45
S85	3	S84 and (comput\$4 or calculat\$4 or determin\$4) same (ECC or error near4 correction)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/14 16:47
S86	3	S85 and register	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/14 16:50
S87	11	"6906961"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/14 17:02

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S88	3	"6906961".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/14 17:02
S89	579	(transfer\$4 or mov\$4 or transport\$4 or shift\$4) near5 (data or block or packet) near5 flash near memory near5 controller	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/14 20:38
S90	6	S89 and comput\$4 near5 error adj correction near4 code	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/14 20:39
S91	26	S89 and (comput\$4 or calculat\$4 or estimat\$4 or perform\$4) near5 error adj correction near4 code	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/14 20:41
S92	579	S89 and flash near4 memory	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/14 20:51
S93	68	S92 and controller near5 coupled near4 flash near4 memory	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/14 20:51
S94	1	S93 and controller same shift\$4 near4 (data or block or packet) same flash near4 memory same controller	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/14 20:52
S95	1	S93 and controller same shift\$4 near4 (data or block or packet) same flash	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/14 20:53
S96	105	controller same shift\$4 near4 (data or block or packet) same flash near4 memory same controller	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/14 20:53
S97	23	S89 and S96	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/14 20:54

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S98	13	S97 and switch\$4 same register	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/14 21:16
S99	206109	flash adj memory	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/14 21:20
S10 0	8	S99 and controller same shift\$4 same data same flash same memory same (ECC or error adj correction) same register same switch\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/14 21:22
S10 1	0	S93 and controller same shift\$4 same data same flash same memory same (ECC or error adj correction) same register same switch\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/14 21:25
S10 2	0	S89 and controller same shift\$4 same data same flash same memory same (ECC or error adj correction) same register same switch\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/14 21:25
S10 3	1	select\$4 same stor\$4 same ECC same plural\$4 near4 register same switch\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/14 21:26
S10 4	3	stor\$4 same ECC same plural\$4 near4 register same switch\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/14 21:27
S10 5	139461	controller same shift\$4 data same flash near4 memory same controller sane (ECC or error adj correction adj code)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/14 21:31
S10 6	47248	controller same shift\$4 near5 data same flash near4 memory same controller sane (ECC or error adj correction adj code)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/14 21:31
S10 7	3	controller same shift\$4 near5 data same flash near4 memory same controller same (ECC or error adj correction adj code)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/14 21:31

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S10 8	205	(stor\$4 or sav\$4) same (ECC or error adj correction adj code) same register same switch\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/14 21:36
S10 9	130512	S108 and memory coupl\$4 near4 controller	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/14 21:37
S11 0	15	S108 and memory same coupl\$4 near4 controller	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/14 21:37
S11 1	0	stor\$4 near4 (error adj correction adj code or ECC) near4 plural\$4 near3 register near8 using near2 register	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/14 21:45
S11 2	3	stor\$4 near4 (error adj correction adj code or ECC) near4 plural\$4 near3 register near8 register	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/14 21:46
S11 3	13	controller same shift\$4 near4 (data or block or packet) same flash near4 memory same controller same switch\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/14 21:47
S11 4	2	"6353553".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/14 22:05
S11 5	8	S113 and NAND	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/14 23:15
S11 6	3	"6,906,961".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/14 23:23
S11 7	1	S116 and NAND	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/14 23:23

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S11 8	239660	flash adj memory	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/27 22:45
S11 9	115219	S118 and memory near7 control\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/27 22:45
S12 0	5443	S119 and (transfer\$4 or send\$4) near5 data same flash adj memory same control\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/27 22:46
S12 1	260	S120 and (ECC or error adj correction adj code) same block same transfer\$4 same data	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/27 22:48
S12 2	1	S121 and stor\$4 near5 (ECC or error near4 code) same plural\$4 near4 register	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/27 22:50
S12 3	41	S121 and stor\$4 near5 (ECC or error near4 code) same register	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/27 22:51
S12 4	16	S121 and stor\$4 near5 (ECC or error near4 code) same register same switch\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/27 22:51
S12 5	1567	S119 and (transfer\$4 or send\$4) near5 data same flash adj memory same memory adj control\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/27 23:04
S12 6	103	S125 and (ECC or error adj correction adj code) same block same transfer\$4 same data	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/27 23:04
S12 7	51	S125 and (comput\$4 or determin\$4 or generat\$4) same (ECC or error adj correction adj code) same block same transfer\$4 same data	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/27 23:05

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S12 8	1	S127 and stor\$4 near5 (ECC or error near4 code) same register same switch\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/27 23:05
S12 9	8	S127 and (ECC or error near4 code) same register same switch\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/27 23:06
S13 0	8	S129 and (computer or medium)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/27 23:18
S13 1	0	(transferring data block flash memory memory controller computing ECC selectively storing plurality registers switching mechanism ECC occurring concurrently transferring data block)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	SAM E	ON	2010/06/06 22:44
S13 2	0	(transferring data block flash memory memory controller computing ECC selectively storing plurality registers switching mechanism ECC occurring concurrently transferring data block)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	SAM E	ON	2010/06/06 22:44
S13 3	0	(transferring data block flash memory memory controller computing ECC selectively storing plurality registers switching mechanism)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	SAM E	ON	2010/06/06 22:44
S13 4	68	(transferring data block flash memory memory controller computing ECC selectively storing plurality registers switching mechanism)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2010/06/06 22:45
S13 5	0	(transferring data block flash memory memory controller computing ECC selectively storing plurality registers switching mechanism).clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2010/06/06 22:45
S13 6	11539	(714/763,710,746,758,5,6,8).ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/06/06 22:47
S13 7	2	S134 and S136	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/06/06 22:47

## EAST Search History (Prior Art)

S13 8	68	(transferring data block flash memory memory controller computing ECC selectively storing plurality registers switching mechanism)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2010/06/06 22:54
S13 9	6	S138 and "714"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/06/06 22:54
S14 0	6	S139 and flash near4 memory	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/06/06 22:56
S14 1	209	(flash memory controller shift data block flash memory controller compute ECC selectively store ECC plurality registers controller shifts data block)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2010/06/06 23:00
S14 2	0	(flash memory controller shift data block flash memory controller compute ECC selectively store ECC plurality registers controller shifts data block)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	SAM E	ON	2010/06/06 23:00